## ABSTRACT OF THE DISCLOSURE

A clock controller 102 controls a clock generated by a clock generator 101 to determine a clock frequency. A computing device 103 executes a software obtained from a storage 104 in accordance with the clock supplied via the clock controller 102. An exclusive processing section detector 110 detects the start and end of an exclusive processing section which is a section during which an exclusive processing is executed. A clock control judging device 111 commands the clock controller 102 to decrease the clock frequency if the exclusive processing section detector 110 has detected the start of a specific processing section, while commanding the clock controller 102 to decrease the clock frequency if the exclusive processing section detector 110 has detected the end of the specific processing section.